

IN THE  
UNITED STATES PATENT AND TRADEMARK OFFICE

Inventor(s): Steinbach et al.

Serial No.:

Examiner:

Filing Date:

Group Art Unit:

Title: CENTERING A MULTI-BAND VOLTAGE CONTROLLED OSCILLATOR

COMMISSIONER FOR PATENTS  
PO Box 1450  
Alexandria, VA 22313-1450

INFORMATION DISCLOSURE STATEMENT

Sir:

This Information Disclosure Statement is submitted:

- ☒ under 37 CFR 1.97(b), or  
(Within three months of filing national application; or date of entry of national application; or before mailing date of first office action on the merits; whichever occurs last)
- ☐ under 37 CFR 1.97(c) together with either a:  
☐ Statement under 37 CFR 1.97(e), or  
☐ a \$180.00 Processing fee under 37 CFR 1.17(p), or  
(After the CFR 1.97 (b) time period, but before final action or notice of allowance, whichever occurs first)
- ☐ under 37 CFR 1.97 (d) together with a:  
☐ Statement under 37 CFR 1.97(e), and  
☐ a \$180.00 processing fee under 37 CFR 1.17(p).  
(Filed after final action or notice of allowance, whichever occurs first, but before payment of the issue fee)

Please charge to Deposit Account **50-1078** the sum of \$0.00. At any time during the pendency of this application, please charge any fees required or credit any overpayment to Deposit Account **50-1078** pursuant to 37 CFR 1.25.

☒ Applicant(s) submit herewith Form PTO 1449. References identified with an asterisk (\*) were disclosed in Patent Application No. \_\_\_\_\_ filed \_\_\_\_\_, now U. S. Patent No. \_\_\_\_\_, and, as such, copies thereof are not included pursuant to the provisions of 37 CFR 1.98(d).

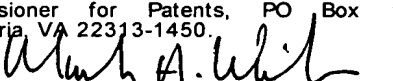
☐ A concise explanation of the relevance of foreign language patents, foreign language publications and other foreign language information listed on PTO Form 1449, as presently understood by the individuals(s) designated in 37 CFR 1.56 (c) most knowledgeable about the content is given on the attached sheet, or where a foreign language patent is cited in a search report or other action by a foreign patent office in a counterpart foreign application, an English language version of the search report or action which indicates the degree of relevance found by the foreign office is listed on form PTO 1449 and is enclosed herewith.

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I hereby certify that this is being deposited with the United States Postal Service "Express Mail Post Office to Addressee" service under 37 CFR 1.10 on the date indicated above and is addressed to: Commissioner for Patents, PO Box 1450, Alexandria, VA 22313-1450.

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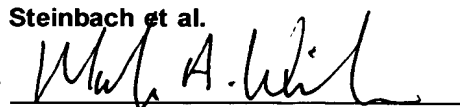


Typed Name: **Mark A. Wilson**

Respectfully submitted,

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By



**Mark A. Wilson**

Attorney/Agent for Applicant(s)

Reg. No. **43,994**

Date: **2/10/2004**

FORM PTO-1449

LIST OF PATENTS AND PUBLICATIONS FOR  
APPLICANT'S INFORMATION DISCLOSURE  
STATEMENT

(Use several sheets if necessary)

ATTY. DOCKET NO.

10030972-1

SERIAL NO.

APPLICANT

Steinbach et al.

FILING DATE

GROUP

## REFERENCE DESIGNATION

## U.S. PATENT DOCUMENTS

EXAMINER INITIAL	*	DOCUMENT NUMBER	DATE	NAME
		6,545,545	4/8/2003	Fernandez-Texon

## FOREIGN PATENT DOCUMENTS

	*	DOCUMENT NUMBER	DATE	NAME	TRANSLATION	
					YES	NO

## OTHER REFERENCES (including Author, Title, Date, Pertinent Pages, etc.)

	DUNNING, GARCIA, LUNDBERG, NUCKOLLS, An all-Digital Phase-Locked Loop with 50-Cycle Lock Time Suitable for High-Performance Microprocessors, IEEE Journal of Solid-State Circuits, Vol. 30, No. 4, April 1995, pp.412-422.
	ANAND, RAZAVI, A 2.75 Gb/s CMOS Clock Recovery Circuit with Broad Capture Range, IEEE ISSCC 2001 Digest, p.214.
	NOGUCHI, TATEYAMA, OKAMOTO, UCHIDA, KIMURA, and TAKAHASHI, A 9.9G-10.8Gb/s Rate-Adaptive Clock and Data-Recovery with No External Reference Clock for WDM Optical Fiber Transmission, IEEE ISSCC 2002 Digest, p.252.

EXAMINER

DATE CONSIDERED

\* Copies of these references are not enclosed pursuant to 37 CFR 1.98(d). (See accompanying IDS)